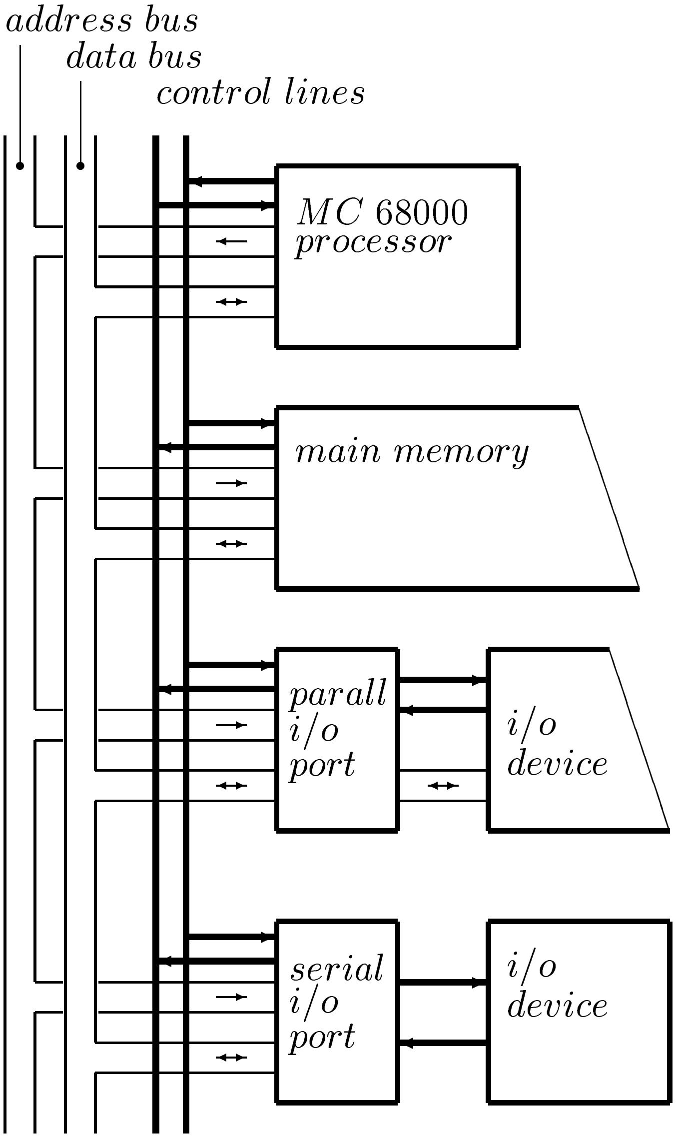
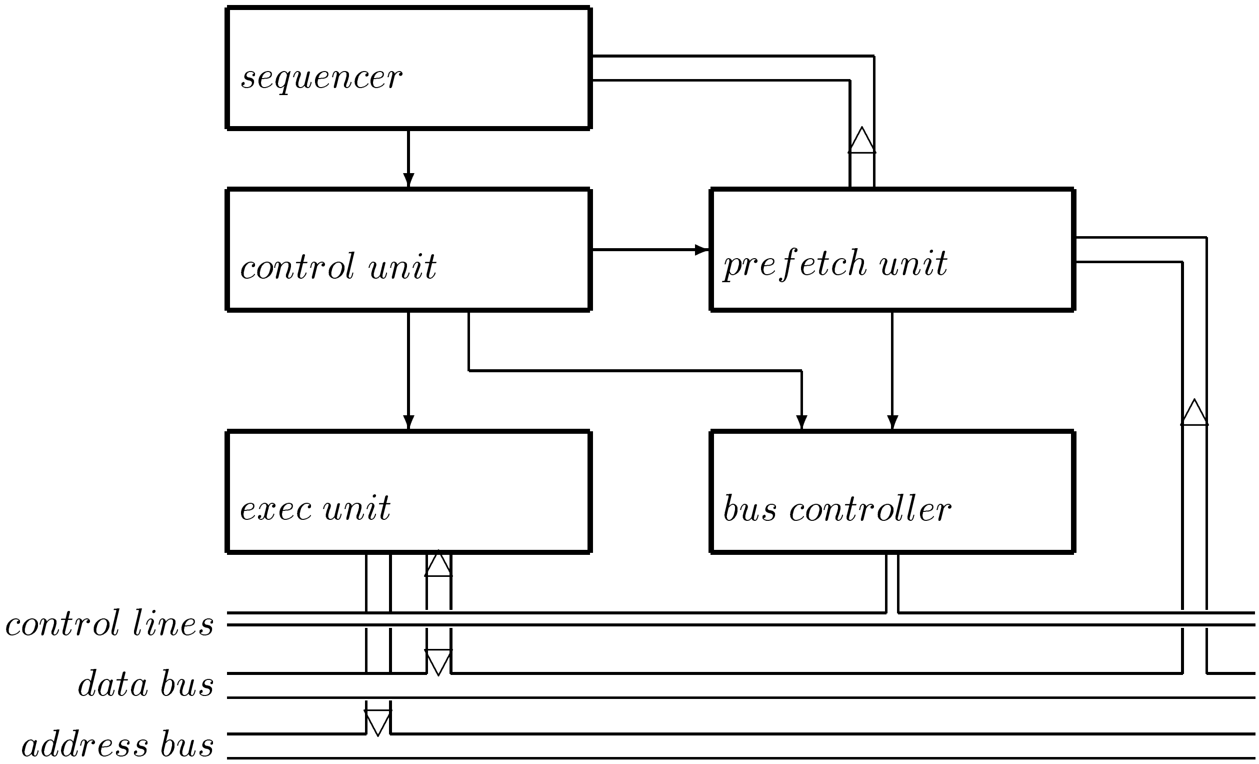
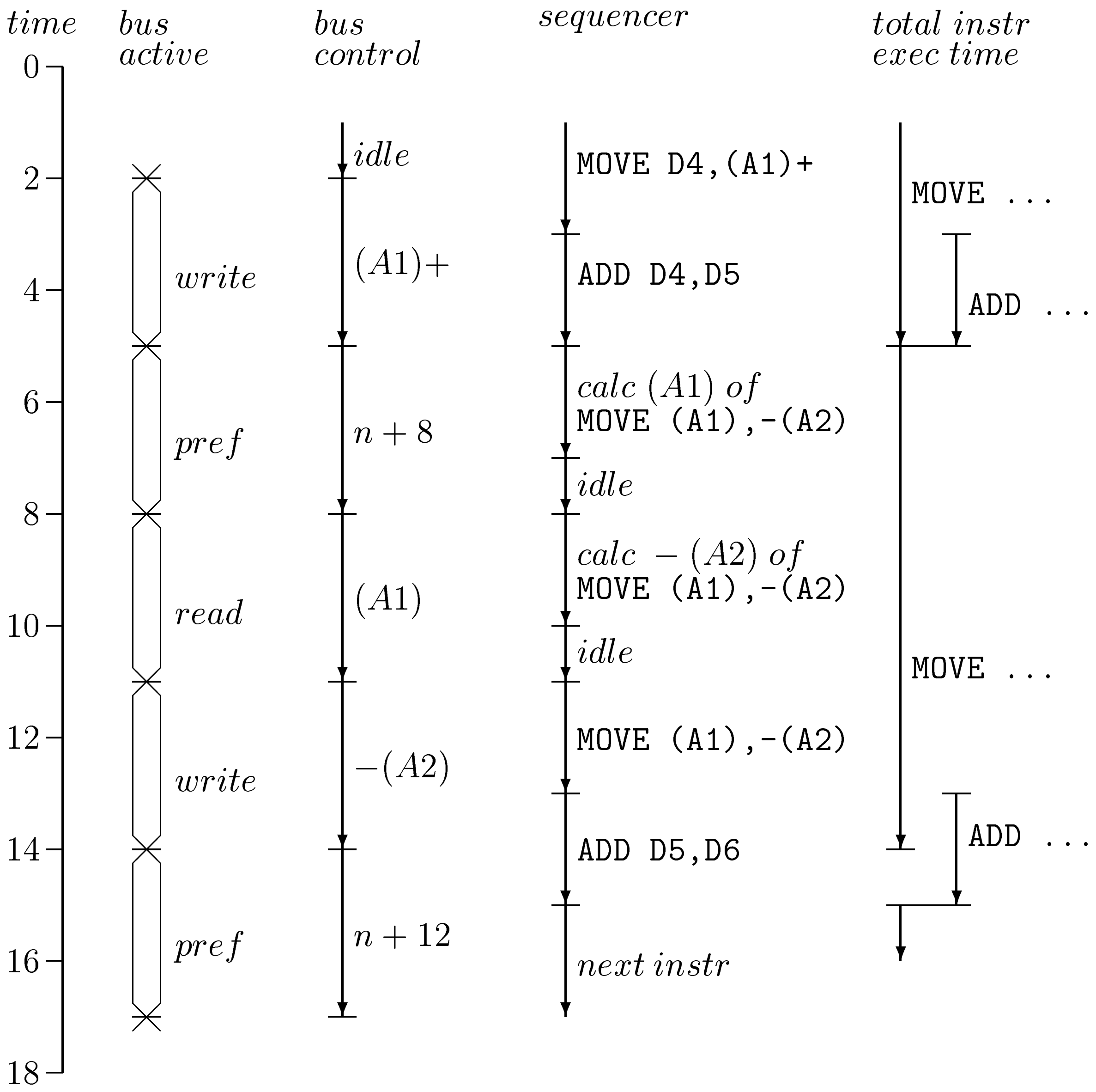


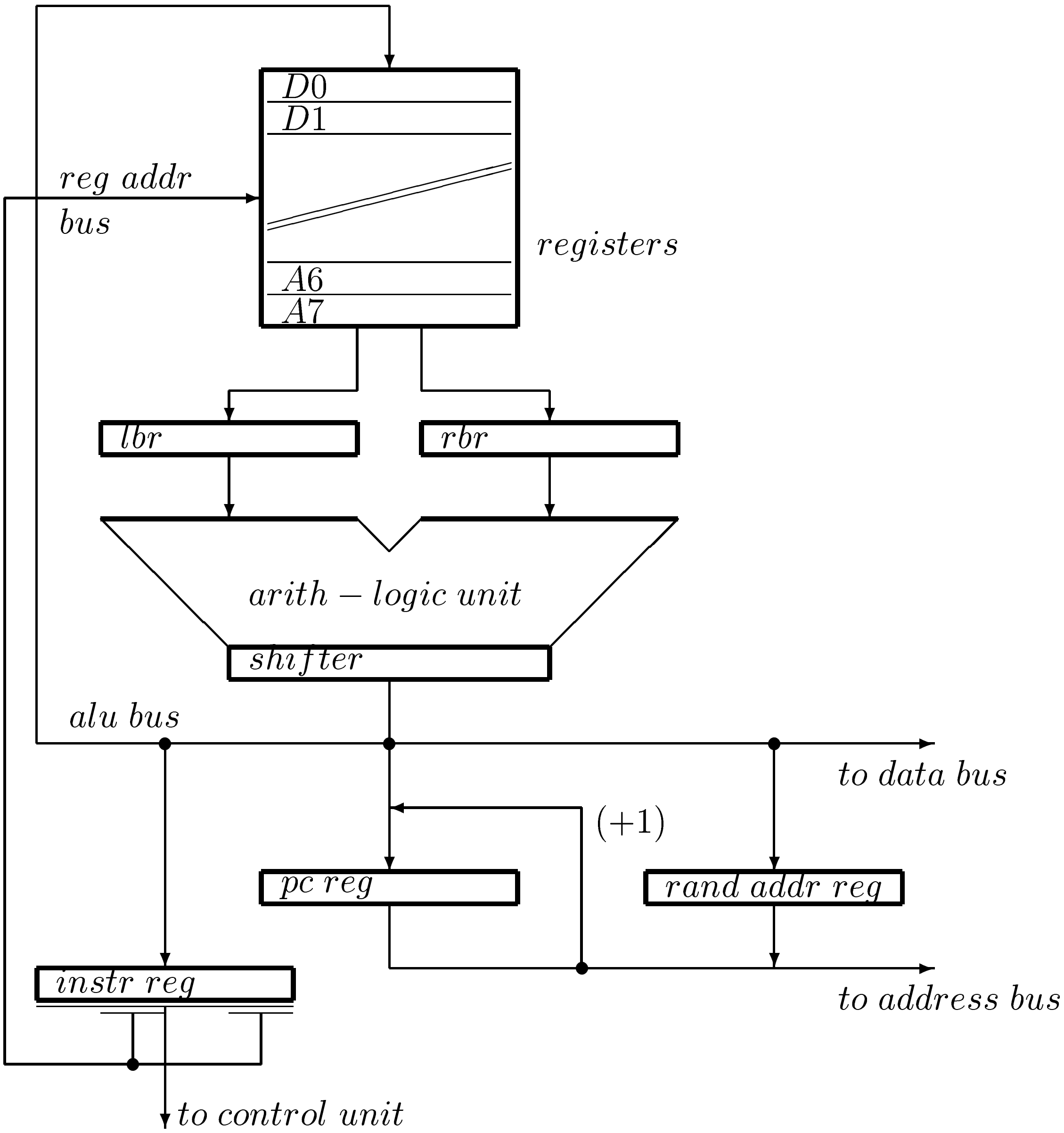
system configuration



The basic processor configuration



Instruction execution timing



The structure of the execution unit