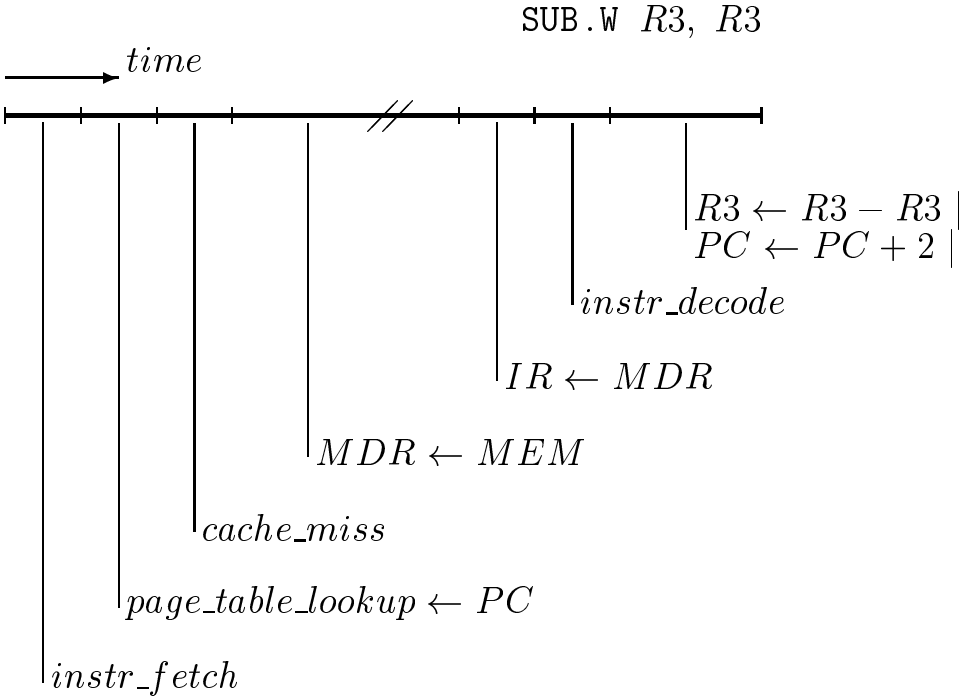
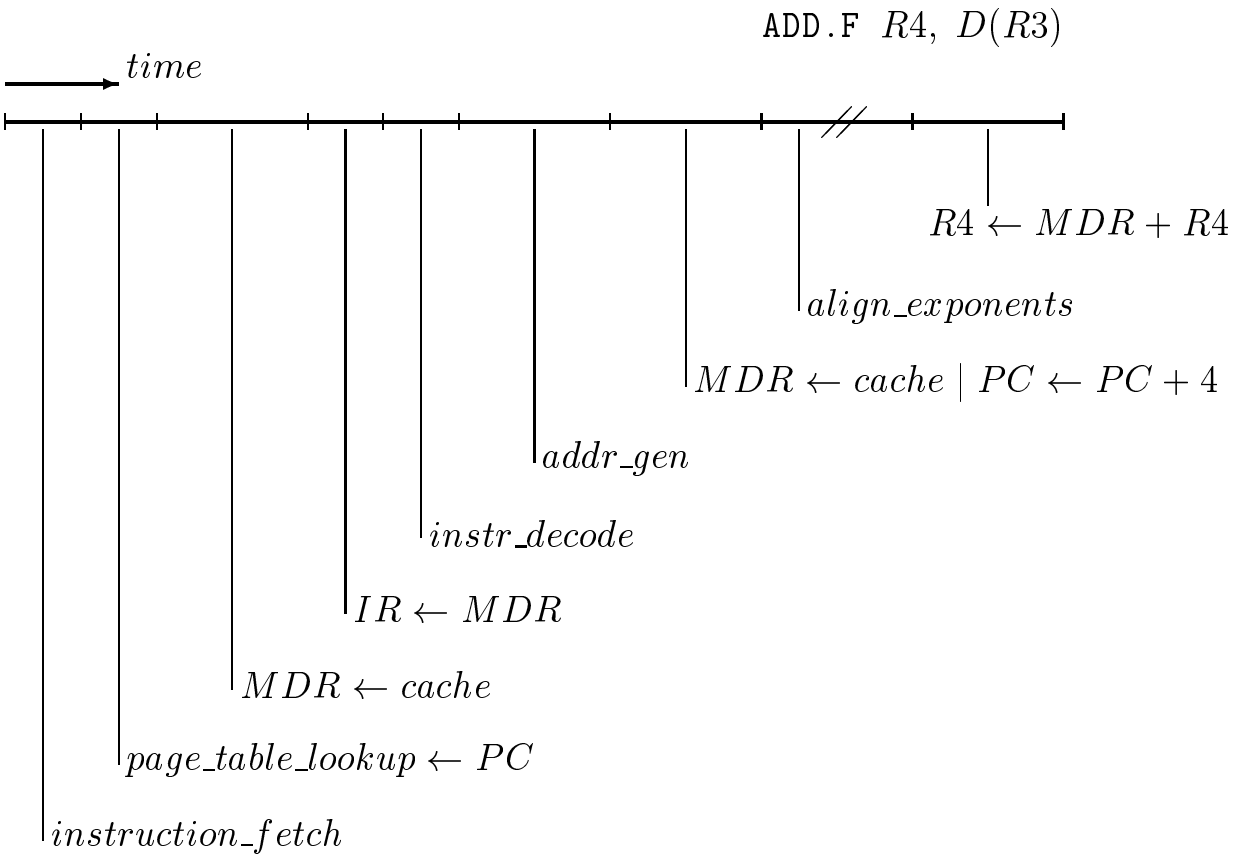

SUB.W R3, R3
time
R3 ← R3 − R3 |
PC ← PC + 2 |
instr_decode
IR ← MDR
MDR ← MEM
cache_miss
page_table_lookup ← PC
instr_fetch


ADD.F R4, D(R3)
time
R4 ← MDR + R4
align_exponents
MDR ← cache | PC ← PC + 4
addr_gen
instr_decode
IR ← MDR
MDR ← cache
page_table_lookup ← PC
instruction_fetch